

CONNECTION	SIGNAL	DESCRIPTION
J1 +	PWR	This pin should be connected to the positive output of the driver power source. The maximum applied voltage should not exceed +50 VDC.
J1 -	GND	This pin should be connected to the negative output of the driver power source.
J4 +	CMD1	The command for Solenoid -1 should be connected to this pin. This input is TTL / CMOS compatible. However, this input must not exceed the voltage applied to J1 +.
J4 -	GND	This pin may be used as the return for CMD1.
J6 +	CMD2	The command for Solenoid -2 should be connected to this pin. This input is TTL / CMOS compatible. However, this input must not exceed the voltage applied to J1 +.
J6 -	GND	This pin may be used as the return for CMD2.
J2 +	PWR	This pin should be connected to one terminal of Solenoid-1.
J2 -	SOL1	This pin should be connected to the other terminal of Solenoid-1
J3 +	PWR	This pin should be connected to one terminal of Solenoid-2.
J3 -	SOL2	This pin should be connected to the other terminal of Solenoid-2.
JP1-6	GND	This pin may be used as the return for serial communications.
JP1-7	TXD	The transmit pin to the micro-controller. The level is zero to +5 VDC.
JP1-8	RXD	The receive pin from the micro-controller. The level should be zero to +5 VDC.

SCSD-01 and SCSD-02 Pin Assignment and Description



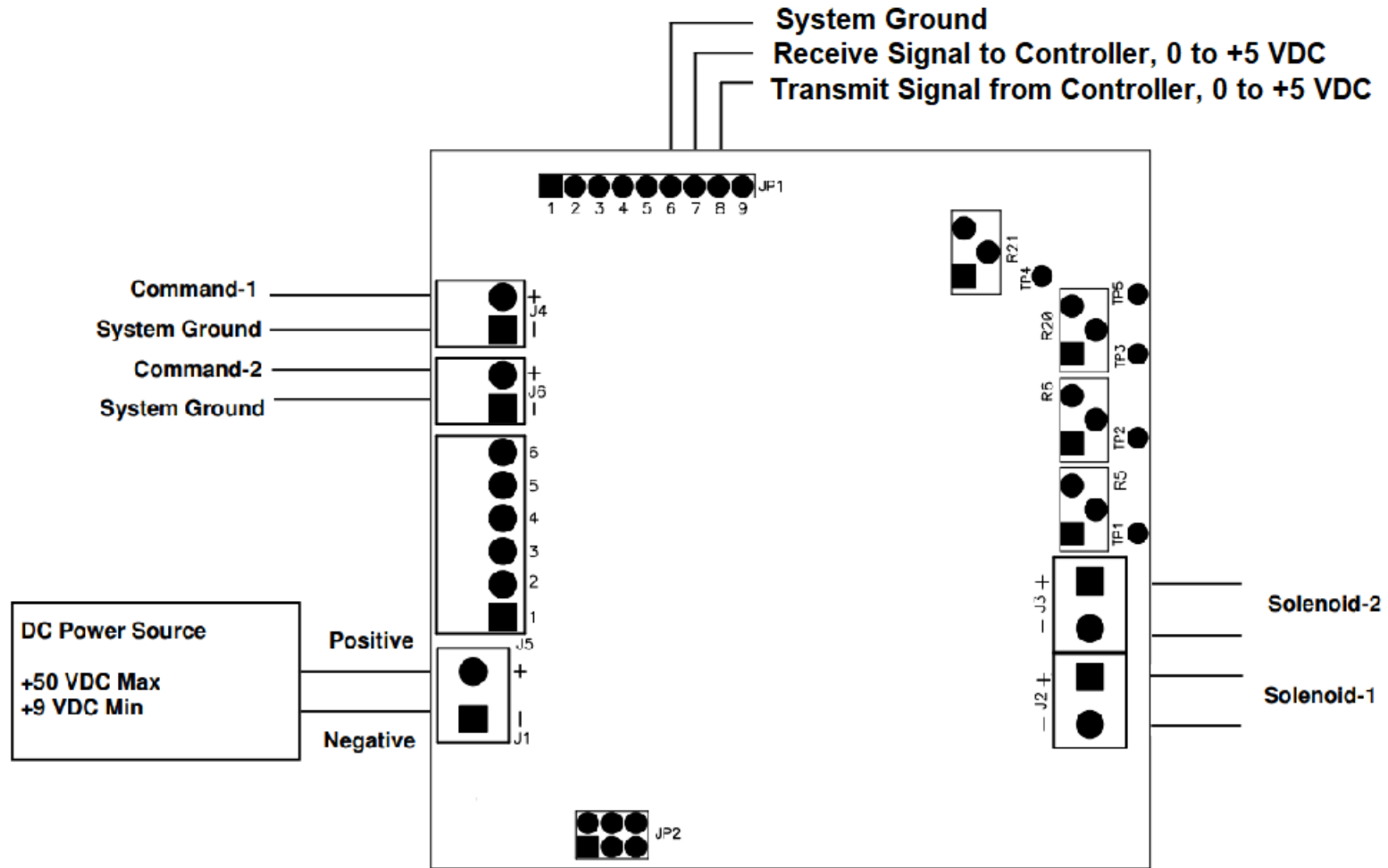
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Warning:

Handling the Serially Controlled Solenoid Driver module shall be performed in a static safe environment while a ground strap is used. Damages arising due to not observing the static pre-cautions shall void the limited ninety-day warranty.



Serially Controlled Solenoid Driver Wiring Diagram



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